

**IN THE CLAIMS:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (Currently Amended) A digital signal recorder for recording a digital signal on a recording medium, comprising:

first key information generation unit to generate at least one item of first key information which is apparatus specific key information;

second key information generation unit to generate at least one item of second key information;

key generation unit which receives said both of said first and second key information generated by said first key information generation unit and said second key information generation unit, and performs a prescribed arithmetic operation thereon to generate a key;

an encryption circuit which receives said key and said digital signal and encrypts said digital signal with said key, and outputs the resulting encrypted digital signal in a case where said digital signal needs copy protection; and

a recording circuit which records at least one of said at least one item of second key information generated by said second key information generation unit, together with said encrypted digital signal in a case where said digital signal needs copy protection, and records said digital signal without encryption in a case where said digital signal needs no copy protection.

2. (Previously Presented) The digital signal recorder according to claim 1, wherein said digital signal has a packet format of a prescribed length.

3. (Currently Amended) The digital signal recorder according to claim 1, wherein:

said second key information generation unit has a function for updating said at least one item of said second key information at a prescribed time interval; and  
said recording circuit has a function for recording information capable of identifying timing when said second key information generation unit updates said key information.

4. (Currently Amended) The digital signal recorder according to claim 3, wherein:

said digital signal has a packet format of a prescribed length; and  
said recording circuit has a function for adding information capable of identifying timing where said second key information generation unit updates said key information, and where such information is added to packets of said digital signal and recorded on said recording medium.

5. (Currently Amended) The digital signal recorder according to claim 1, wherein:

said encryption circuit has a function capable of selecting between a first function for encrypting and outputting said digital signal, and a second function for

outputting said digital signal as is without encryption; and

    said recording circuit has a function for recording, in a prescribed area on said recording medium, encryption flag information indicating whether or not said digital signal is encrypted, and, when not encrypted, not recording said second key information.

6. (Previously Presented) The digital signal recorder according to claim 5, wherein:

    said digital signal has a packet format of a prescribed length; and  
    said recording circuit has a function for adding encryption flag information indicating whether or not said digital signal is encrypted, to packets of said digital signal, and a function for recording on said recording medium.

7. (Currently Amended) A digital signal recorder in which a digital signal of a packet format of a prescribed length is input and divided into other prescribed lengths; a synchronization signal, recording information signal, auxiliary information signal, and first error correction code are added thereto to define a block format; one track is formed by a prescribed number of blocks thus made; a second error correction code is added in units of n tracks (where n is an integer 1 or greater); said second error correction code is also divided and said first error correction code is added thereto to constitute a block format; and said tracks are recorded on said recording medium; comprising:

first key information generation unit to generate at least one item of first key information which is apparatus specific key information;

second key information generation unit to generate at least one item of second key information;

key generation unit to receive both of said first and second key information generated by said first key information generation unit and said second key information generation unit, and to perform a prescribed arithmetic operation to generate a key;

an encryption circuit which receives said key and said digital signal, encrypts said digital signal with said key, and outputs the resulting encrypted digital signal in a case where said digital signal needs copy protection; and

a recording circuit which records ~~at least one of~~ said at least one item of second key information generated by said second key information generation unit, together with said encrypted digital signal in a case where said digital signal needs copy protection, and records said digital signal without encryption in a case where said digital signal needs no copy protection.

8. (Currently Amended) The digital signal recorder according to claim 7, wherein said recording circuit has a function for holding said second key information in an auxiliary information signal area in said blocks and recording same on said recording medium.

9. (Currently Amended) The digital signal recorder according to claim 7, wherein said second key information generation unit has a function for updating said at least one item of said second key information at a prescribed time interval; and said recording circuit has a function for recording information capable of identifying

timing where said second key information generation unit updates said key information, in a prescribed area on said recording medium.

10. (Previously Presented) The digital signal recorder according to claim 9, wherein said recording circuit has a function for holding said information capable of identifying said timing in a recording information signal area in said blocks and recording same on said recording medium.

11. (Previously Presented) The digital signal recorder according to claim 9, wherein said recording circuit has a function for holding said information capable of identifying said timing in an auxiliary information signal area in said blocks and recording same on said recording medium.

12. (Previously Presented) The digital signal recorder according to claim 9, wherein said recording circuit has a function for adding said information capable of identifying said timing to packets in said digital signal and recording same on said recording medium.

13. (Currently Amended) The digital signal recorder according to claim 9, wherein said second key information generation unit has a function for updating said key information at points of separation between units of n tracks wherewith said second error correction code was added.

14. (Currently Amended) The digital signal recorder according to claim 7,

wherein:

    said encryption circuit has a function for encrypting and outputting said digital signal; and a function for outputting same as is, without encryption; and

    said recording circuit has a function for recording encryption flag information indicating whether or not said digital signal is encrypted, in a prescribed area on said recording medium, and, when not encrypted, not recording said second key information.

15. (Previously Presented) The digital signal recorder according to claim 14, wherein said recording circuit has a function for holding said encryption flag information in recording information signal area of said blocks and recording same on said recording medium.

16. (Previously Presented) The digital signal recorder according to claim 14, wherein said recording circuit has a function for holding said encryption flag information in auxiliary information signal area of said blocks and recording same on said recording medium.

17. (Previously Presented) The digital signal recorder according to claim 14, wherein said recording circuit has a function for adding said encryption flag information to packets in said digital signal.

18. (Previously Presented) The digital signal recorder according to claim 14, wherein said encryption circuit has a function for switching to determine whether or

not to encrypt said digital signal, at points of separation between units of n tracks  
wherewith said second error correction code was added.

19. - 46. (Canceled)